



Patent Application No. 09/871,596 Attorney Docket No. 81754.0061

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<u>d. Ofler, Reg. No. 41,232</u>

Date

Commissioner for Patents

Alexandria, VA 22313-1450

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Art Unit:

Examiner:

2133

Joseph D. Torres

P.O. Box 1450

February 20, 2004
Date of Deposit

In re application of:

Noriyuki Saruhashi

Serial No: 09/871,596

Filed:

May 30, 2001

For:

METHOD FOR TESTING

PHYSICAL LAYER DEVICE AND TRANSMISSION/RECEPTION CIRCUIT WITH TEST CIRCUIT

PETITION FOR EXTENSION OF TIME

Mail Stop Non-Fee Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

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MAR 0 2 2004

Dear Sir:

Technology Center 2100

In accordance with 37 C.F.R. 1.136, Applicant respectfully petitions the Commissioner for a one-month extension of time extending to February 20, 2004, the period for response to the Office Action dated October 20, 2004. A check for \$110 is enclosed. The responsive paper(s) are attached.

Please charge any insufficiency or credit any overpayment to Deposit Account No. 50-1314. A copy of this petition is enclosed.

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Date: February 20, 2004

Respectfully submitted,

HOGAN A HARTSON, L.L.P

by: _______

Anthon# J/Orler

Registration No. 41,232 Attorney for Applicant(s)

500 South Grand Avenue, Suite 1900 Los Angeles, California 90071

Phone: 213-337-6700

Fax: 213-337-6701